

Atty. Docket No. 04860.P0686C2

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
Eric C. Anderson, et al. )  
Application No. 09/007,019 )  
Filed: January 14, 1998 )  
For: EXECUTION CONTROL FOR )  
PROCESSOR TASKS )  
Asst. Commissioner for Patents  
Washington, D.C. 20231

Examiner: Donaghue, L.

Art Unit: 2154

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Technology Center 2100

PRELIMINARY AMENDMENT

Preliminary to examination of the present application, Applicant respectfully requests that the Examiner enter the following amendments.

IN THE SPECIFICATION

*Sub E1 Pending* After the title, please insert the following:

~~RELATED APPLICATIONS~~

*Sub E1 Pending* The present application is a continuation of U.S. Patent Application serial number 08/478,413 filed on June 7, 1995, now abandoned, which is a divisional of U.S. Patent Application serial number 07/954,970, now U.S. Patent number , filed on September 30, 1992.

IN THE CLAIMS

Please amend the claims as follows:

- Sub E1 Pending*
1. (Amended) A method in a computer system of executing a first sequence of modules in a first task, said first sequence of modules linked to one another and having at least one sequence of execution, comprising:
    4. a. storing in each of said first sequence of modules a skip value indicating a next module in said sequence of modules to execute;
    5. b. executing a first module of said first sequence of said modules; and